



MBE Growth and Characterization of GaN HFETs

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GaN HFET

2DEG growth summary

AlN Passivation Process

Conductance DLTS

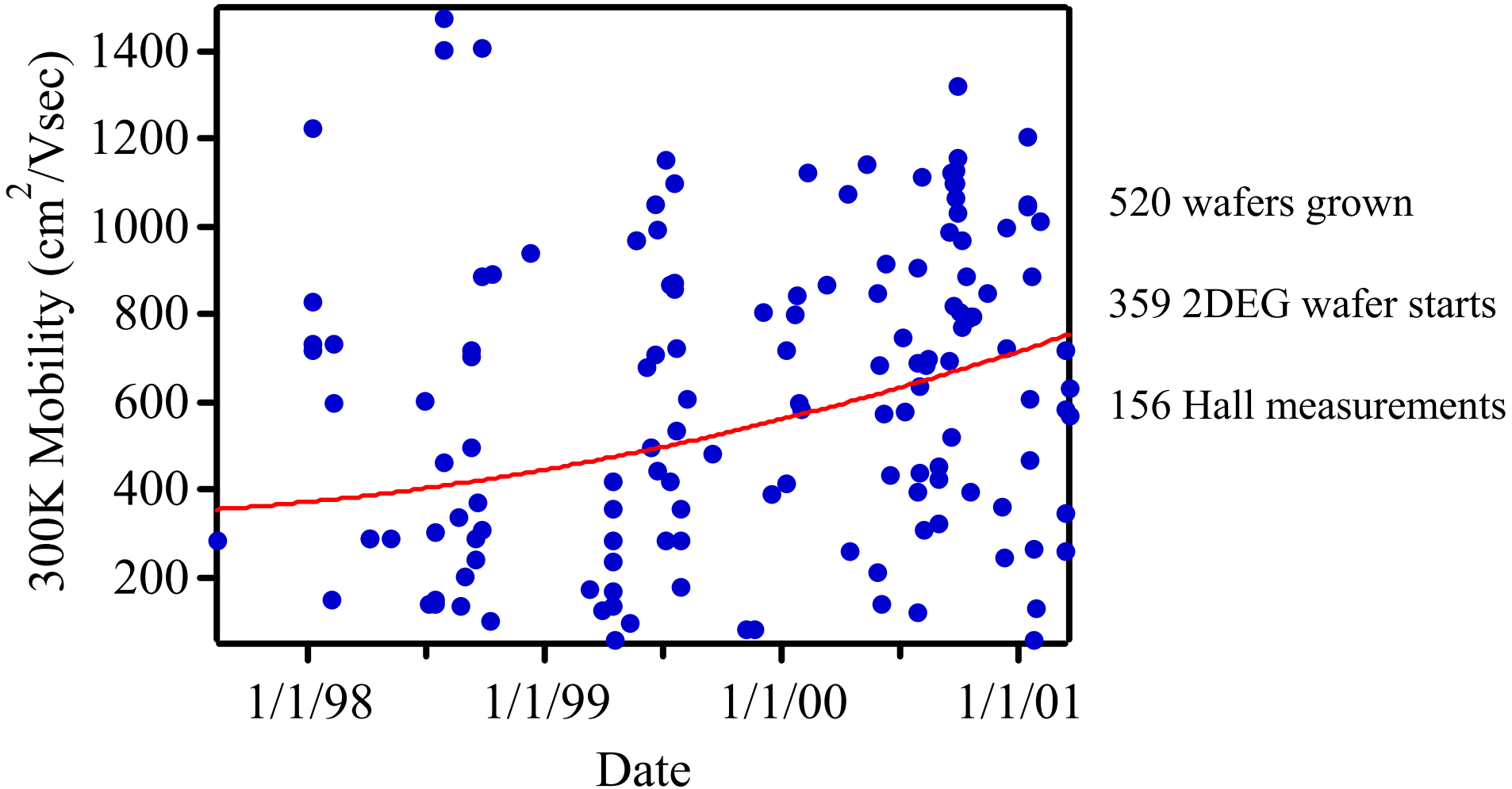
High Al mole fraction barriers

Future for MBE grown HFETs

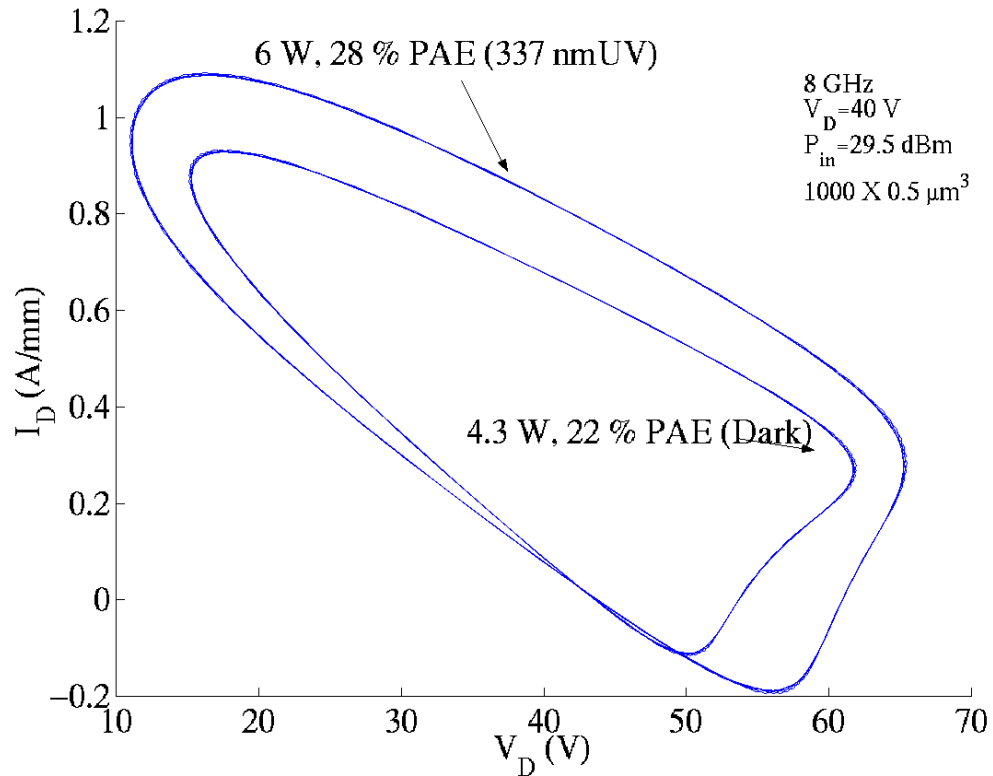


Progress 1996-2001

Mobility vs. Date for 2DEG structures



8 GHz Dynamic Load Line for 1mm HFETs on sapphire



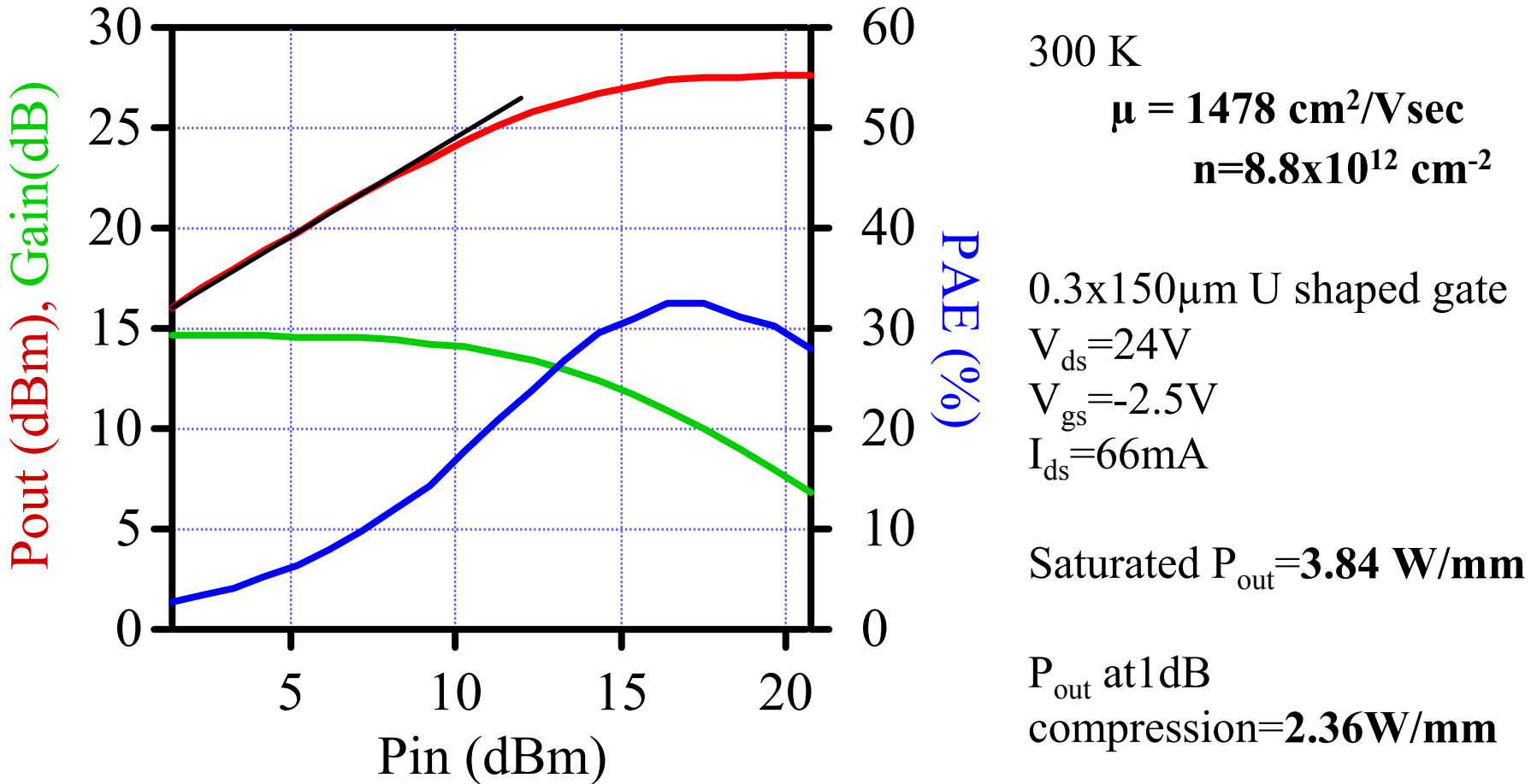
8 GHz
 $V_D = 40$ V
 $P_{in} = 29.5$ dBm
 $1000 \times 0.5 \mu\text{m}^3$

UV illumination lowers sheet resistance in ungated region while increasing sheet carrier concentration:

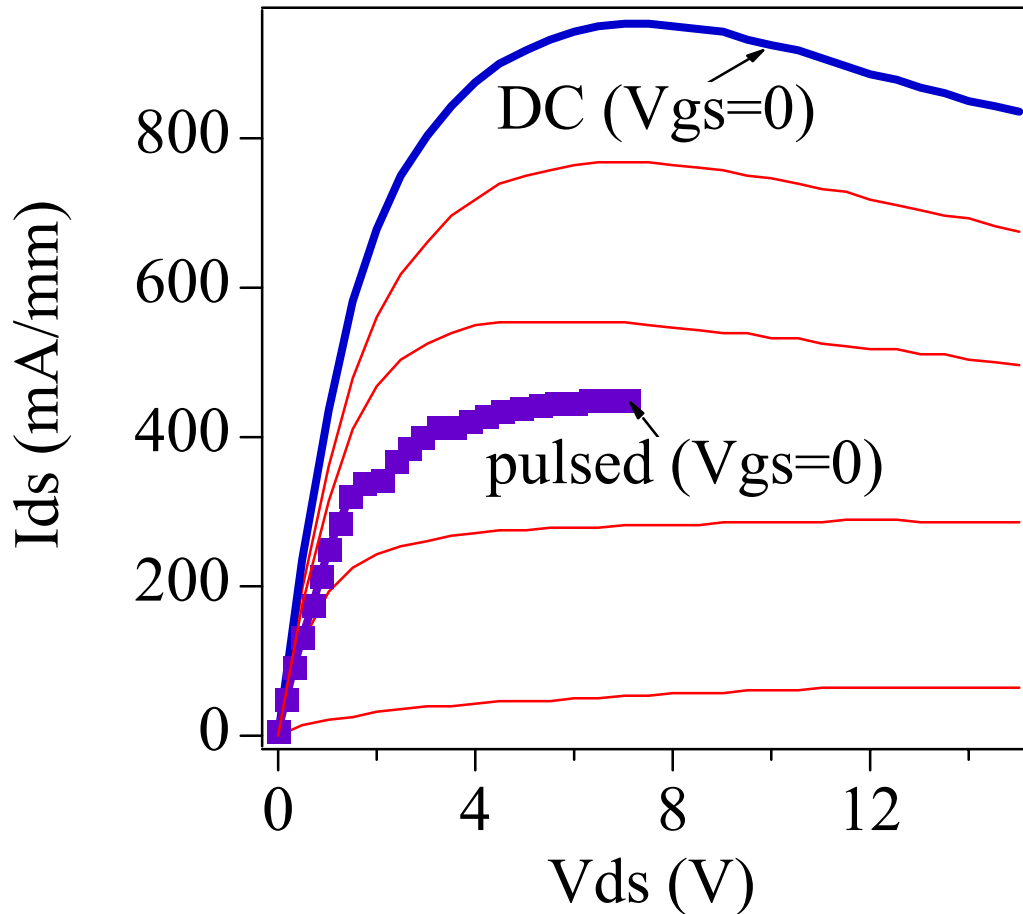
Higher realized current
Increase in PAE, increase in P_{out}

5 μm undoped GaN buffer

MBE HFET (w/ Si_3N_4 passivation) at 7GHz



DC and Pulsed IV for unpassivated MBE HFET



300K

$\mu = 1450 \text{ cm}^2/\text{Vsec}$

$n_s = 1.1 \times 10^{13} \text{ cm}^{-2}$

$0.3 \times 100 \mu\text{m}$

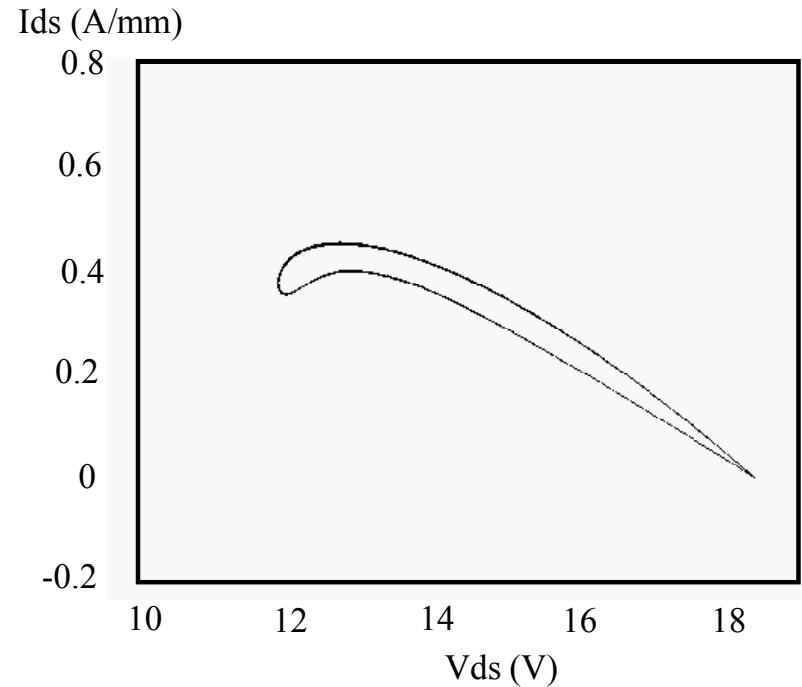
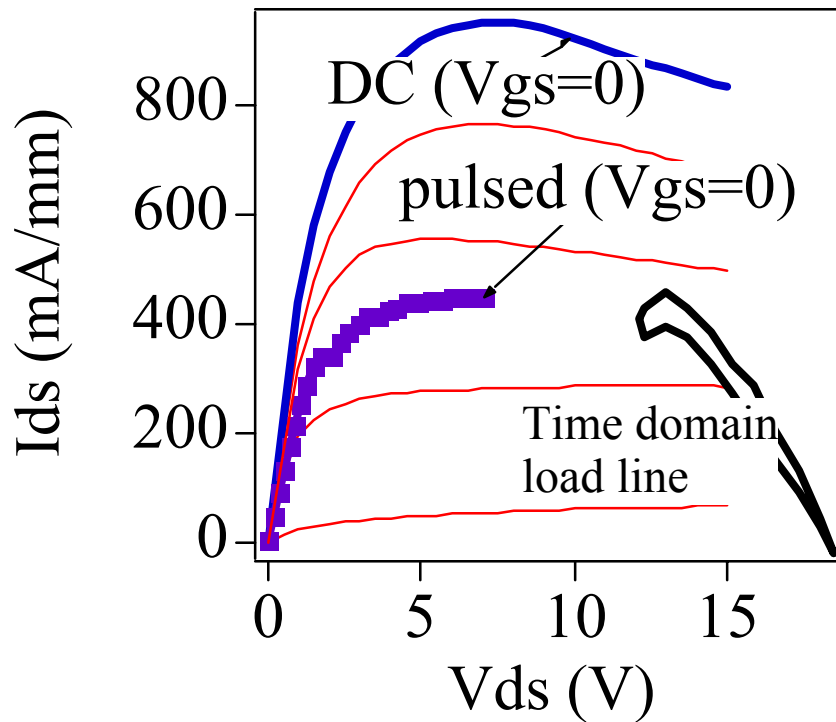
V_{ds} Breakdown 60V

f_t 44.6 GHz

8 GHz 2.1 W/mm

$1 \mu\text{m}$ undoped GaN buffer

DC, pulsed and 8 GHz Real Time Load Line of MBE grown HFET





AlN as a post-process passivation

AlN is deposited on processed HFETs in the MBE machine as an alternative to Si_3N_4

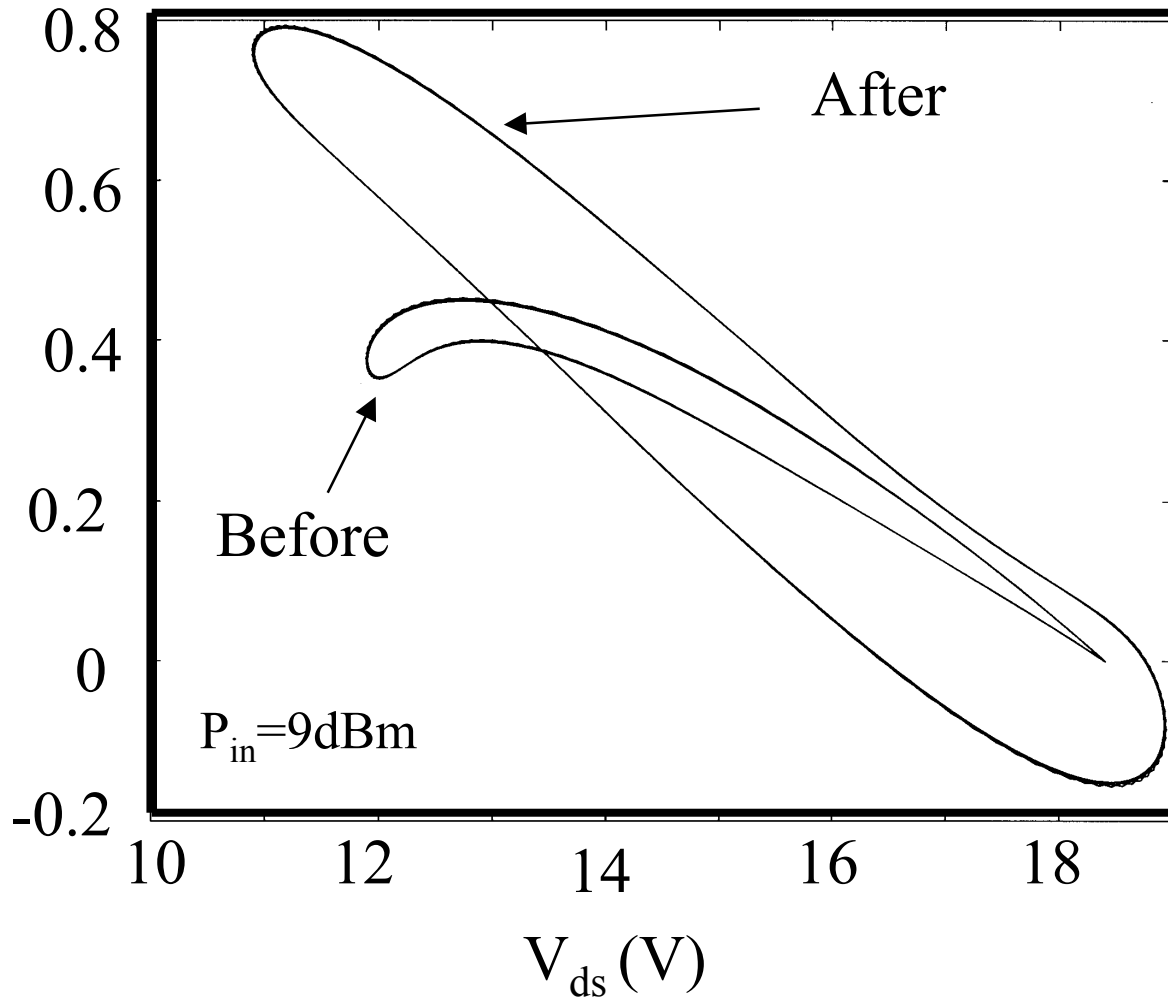
Growth is by Migration Enhanced Epitaxy (MEE) at 150°C

MEE is used to attempt a better gate sidewall coverage than would occur for MBE growth

Real time load-line before and after AlN

Passivation Process

I_{ds} (A/mm)

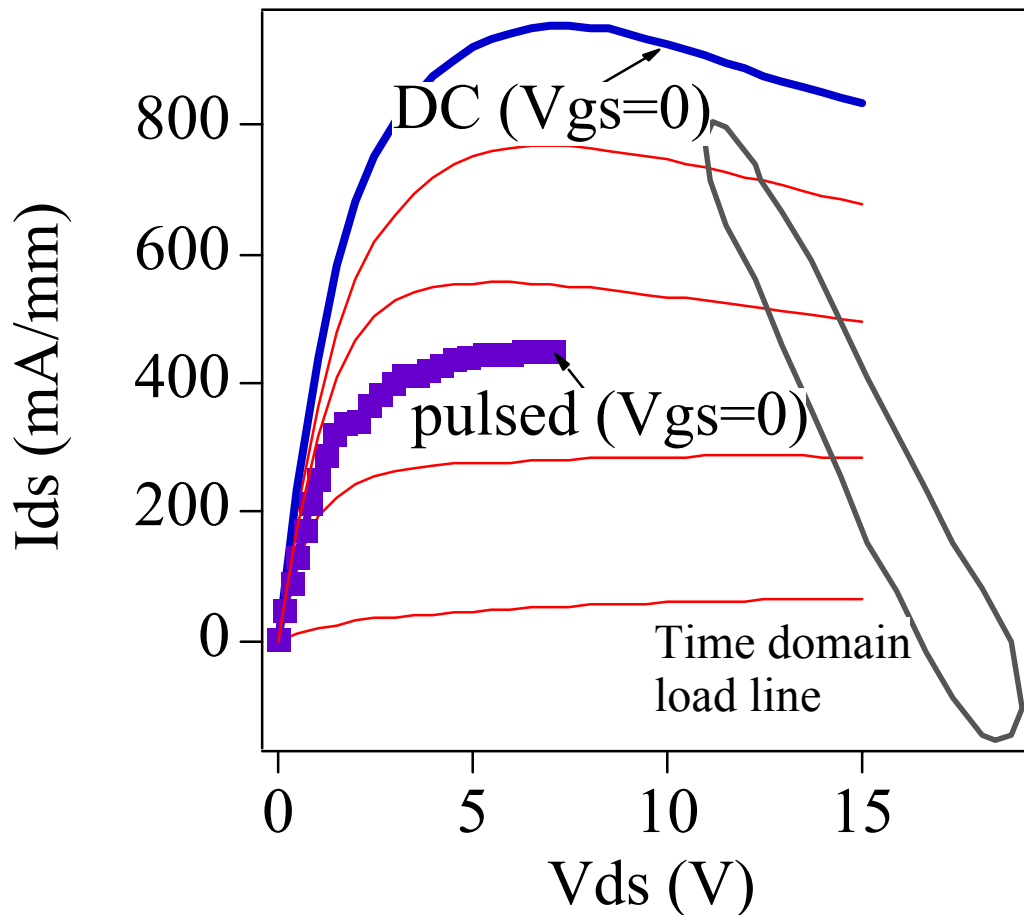


Nearly full DC channel current is available at 8 GHz

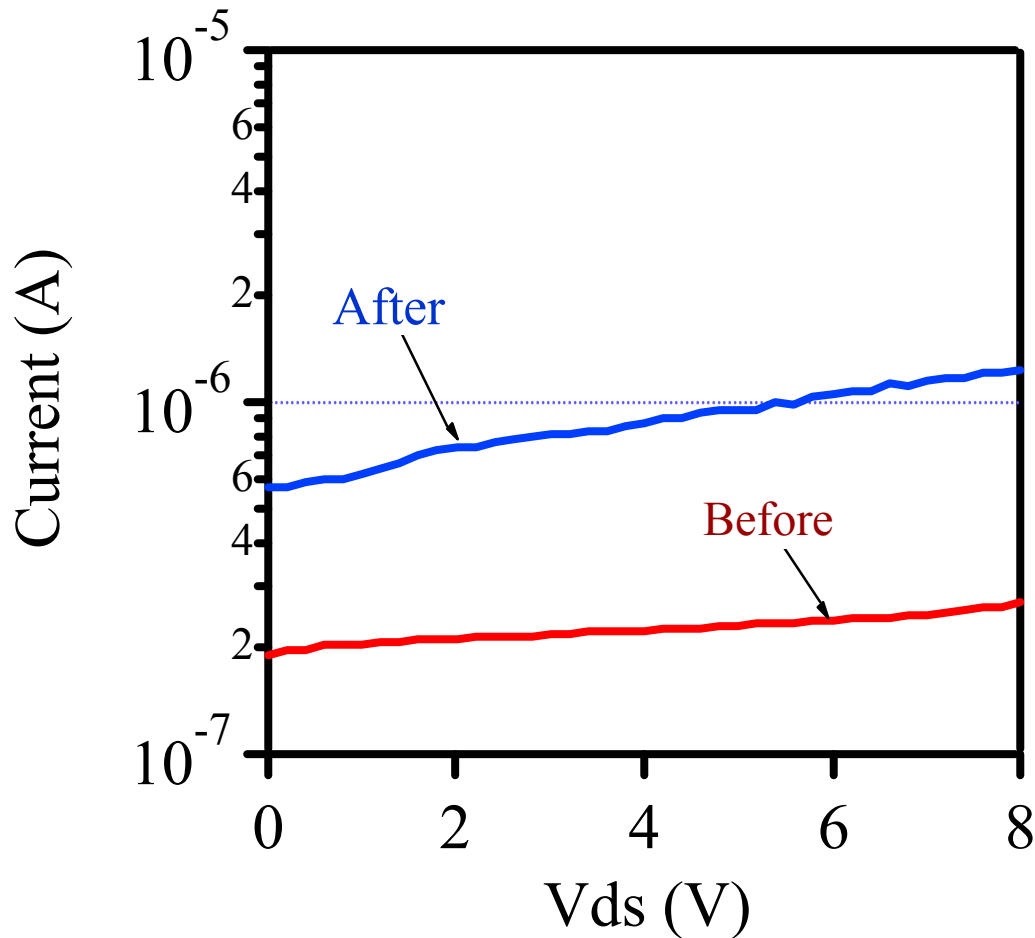
High Power, Broadband, Linear, Solid State Amplifier



DC, pulsed and time domain load line for MBE HFET with AlN Passivation Process

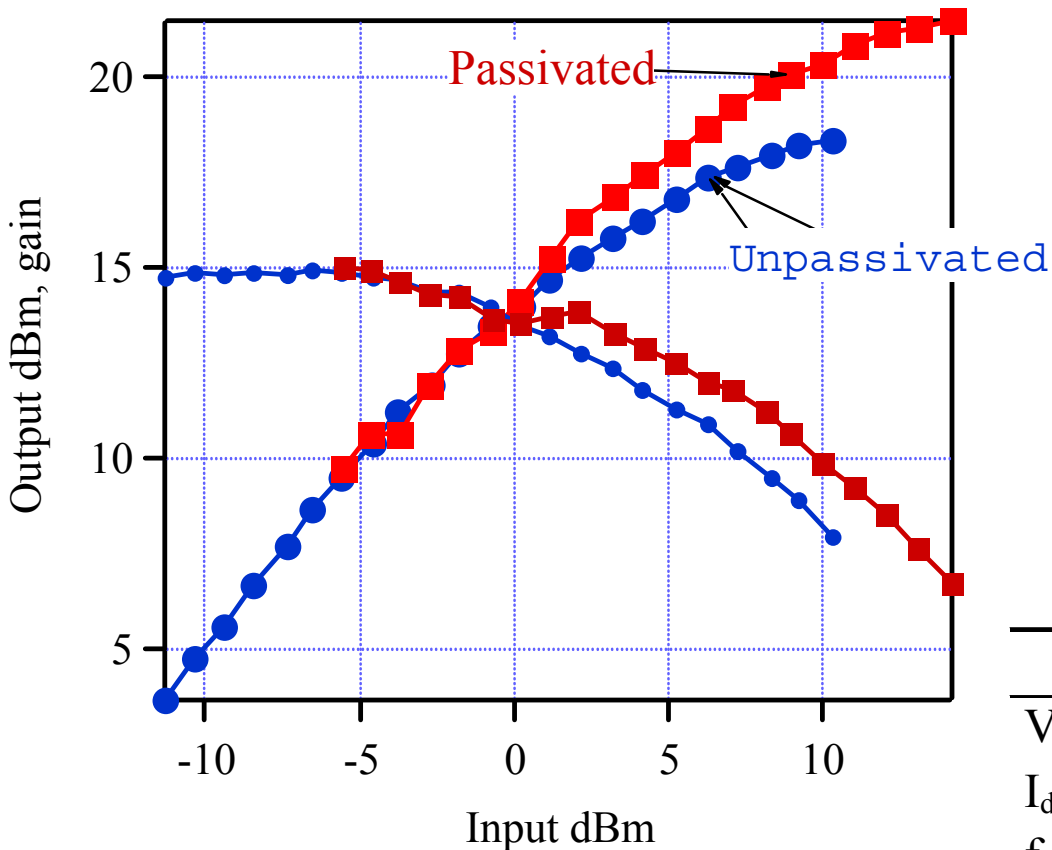


Gate Leakage Before and After AlN Passivation Process



V_{gs} = -6V

AlN Passivation Process



MBE growth w/ 1 μm GaN buffer

0.3x100 μm T shaped gate

$V_{ds} = 15\text{V}$

$V_{gs} = -2.0\text{V}$

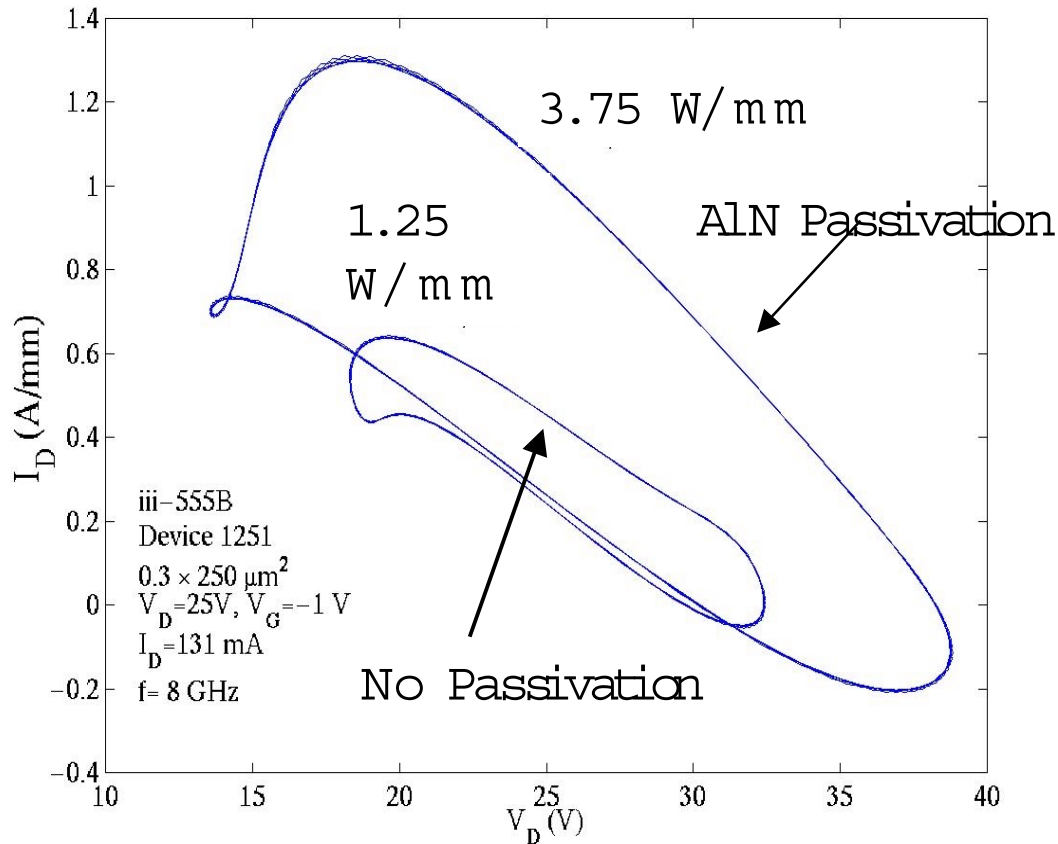
$I_{ds} = 26\text{mA}$

RF data is extracted from time domain analysis - no illumination

Nearly 3dB improvement in gain near saturation is seen

	<i>Before</i>	<i>After</i>
Vds breakdown	60 V	45 V
I_{dss}	1.0 A/mm	1.0 A/mm
f_t	44.6 Ghz	40 Ghz
-1dB compression	.24 W/mm	.42 W/mm

Effect of AlN Passivation on 8 GHz Dynamic Loadlines for OMVPE HFET

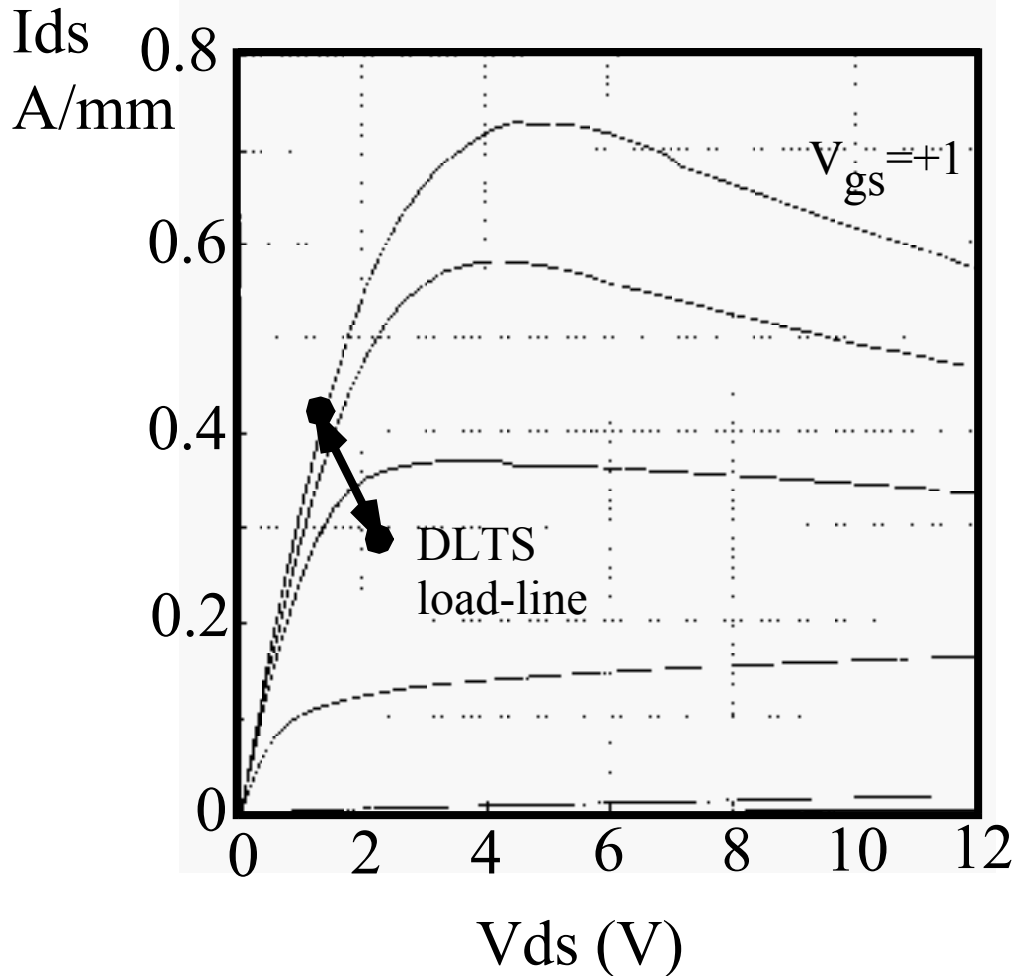


Power output increases by a factor of 3 for 150\AA AlGaN barrier compared to a factor of 2 for 300\AA barrier

High Power, Broadband, Linear, Solid State Amplifier



MBE grown unpassivated HFET used for conductance DLTS



300K

$\mu = 1019 \text{ cm}^2/\text{Vsec}$

$n_s = 1.3 \times 10^{13} \text{ cm}^{-2}$

$0.4 \times 200 \mu\text{m}$

$g_m = 230 \text{ mS/mm}$

$I_{gs} \sim 20 \mu\text{A}$

$f_t = 25.4 \text{ GHz}$

$f_{\text{max}} = 40.1 \text{ GHz}$

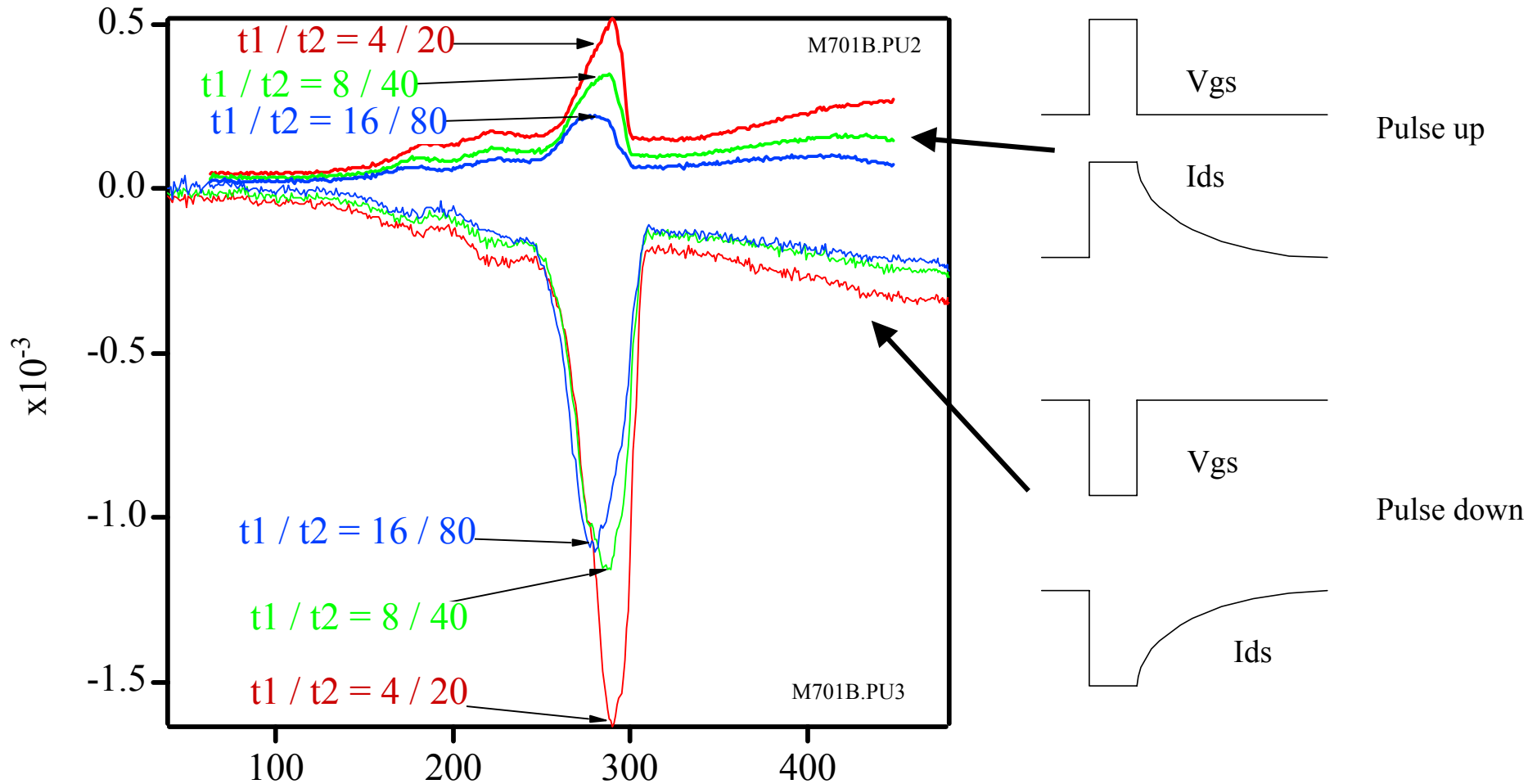
8 GHz 1.7 W/mm

@15V V_{ds}

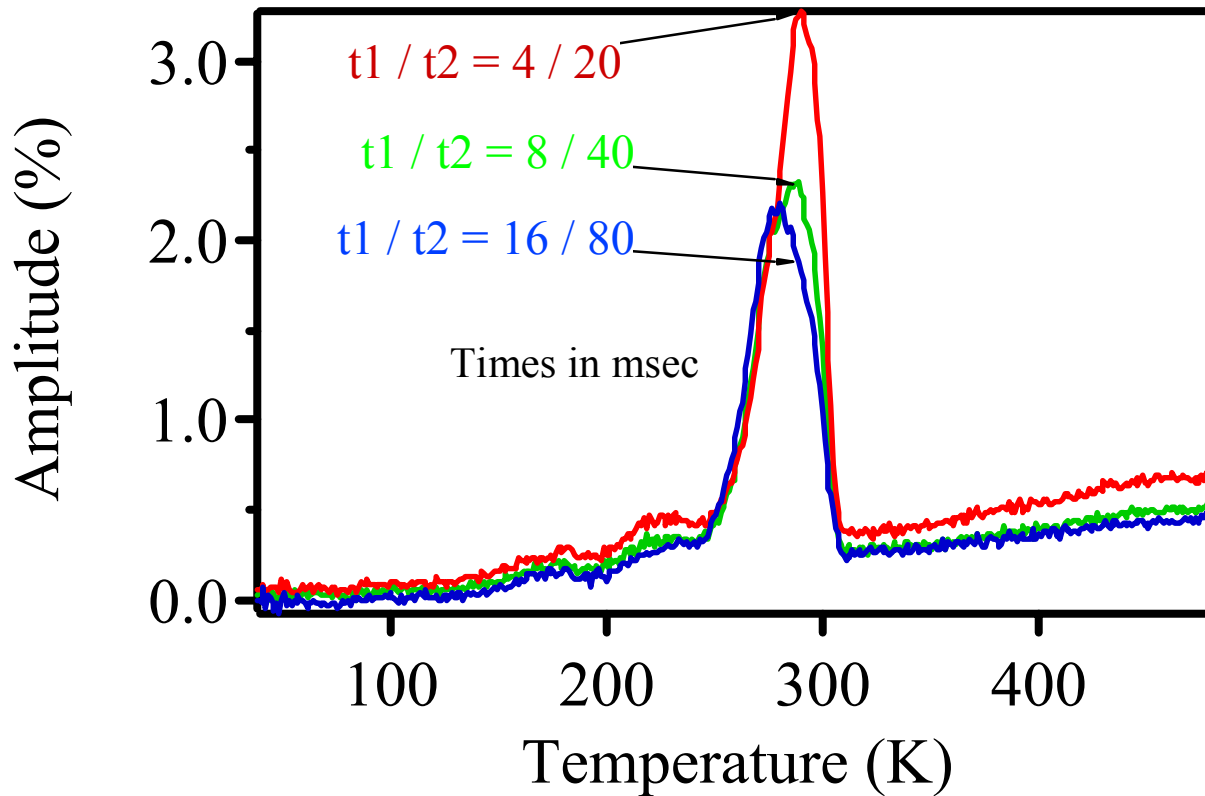
High Power, Broadband, Linear, Solid State Amplifier



Comparison of gate pulse-up vs. pulse-down transient responses for unpassivated MBE grown HFET



Boxcar Plot for Conductance DLTS of MBE Grown Unpassivated HFET

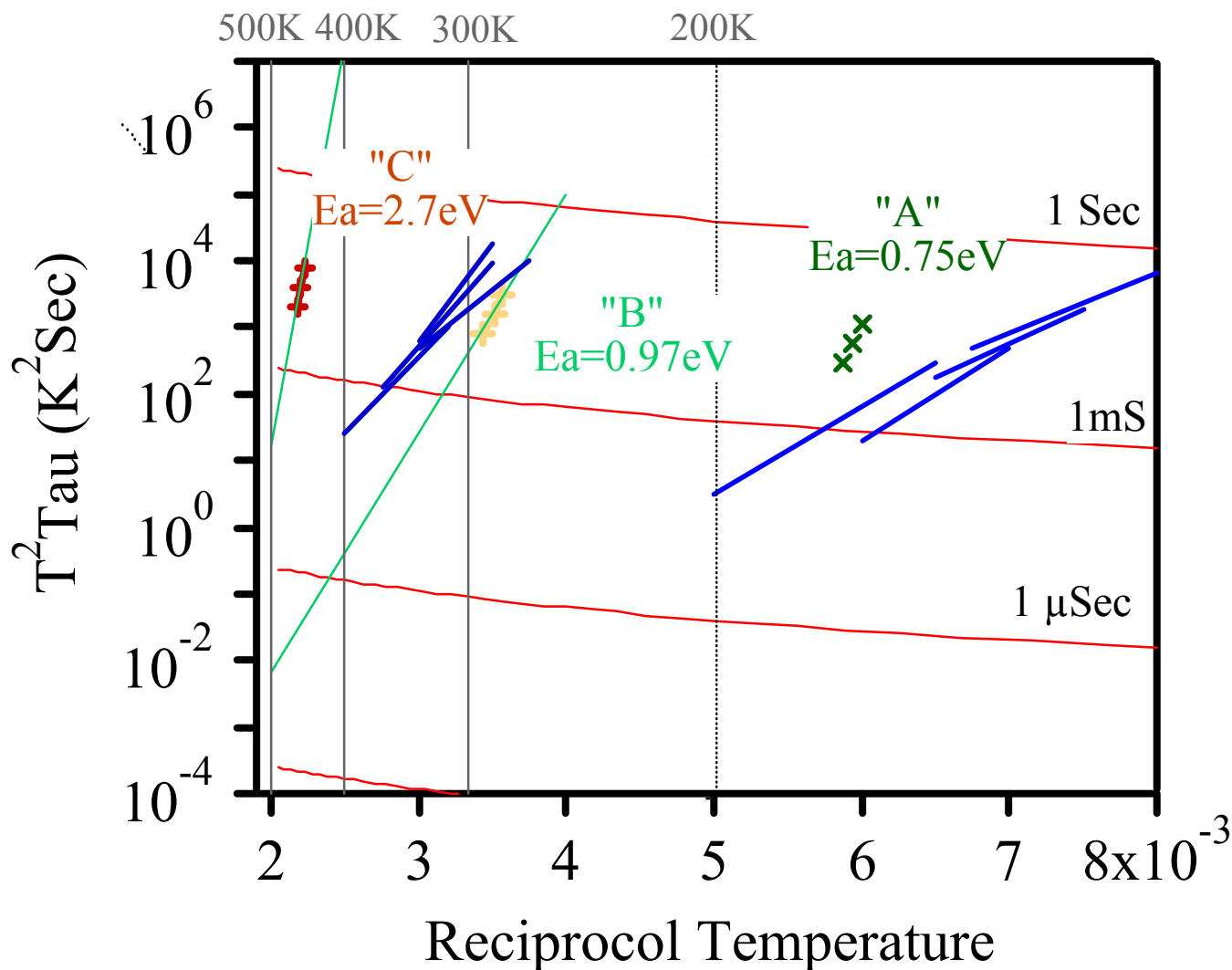


Amplitude is drain-source current transient signal normalized to DC current (15mA) at -2V V_{gs}

Peak heights should be identical for these constant ratios of t_1/t_2 for exponential transient decays

- Variation is indication that decays are slightly non-exponential
- Simple bulk model might not apply

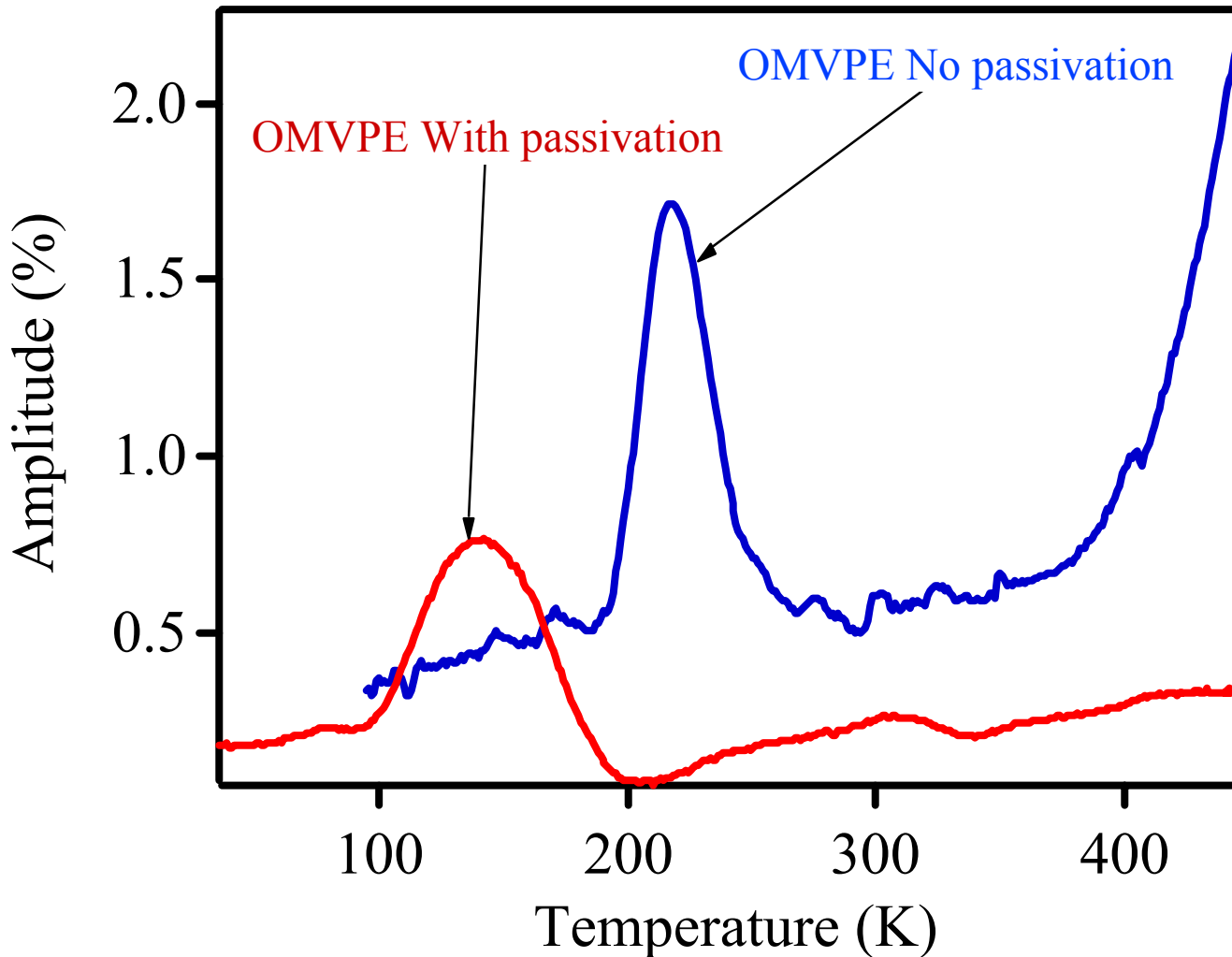
Activation Energy Analysis of GaN HFET DLTS transients



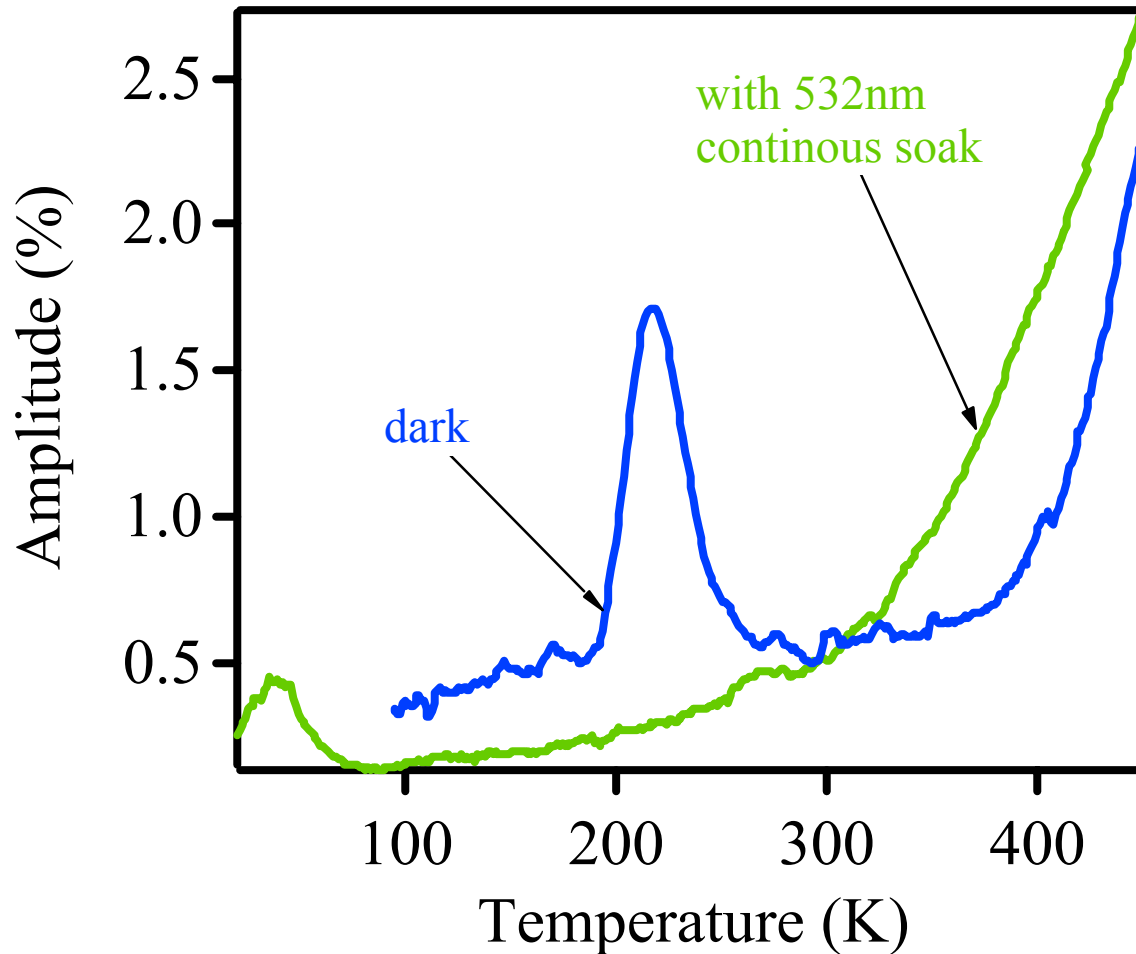
Blue curves are literature values for electron traps in GaN

Other symbols and fits are for unpassivated MBE GaN HFETs

Comparison of DLTS of OMVPE HFETs with and without Si_3N_4 passivation

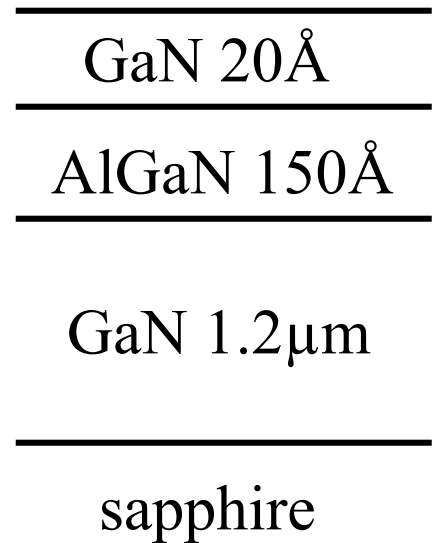
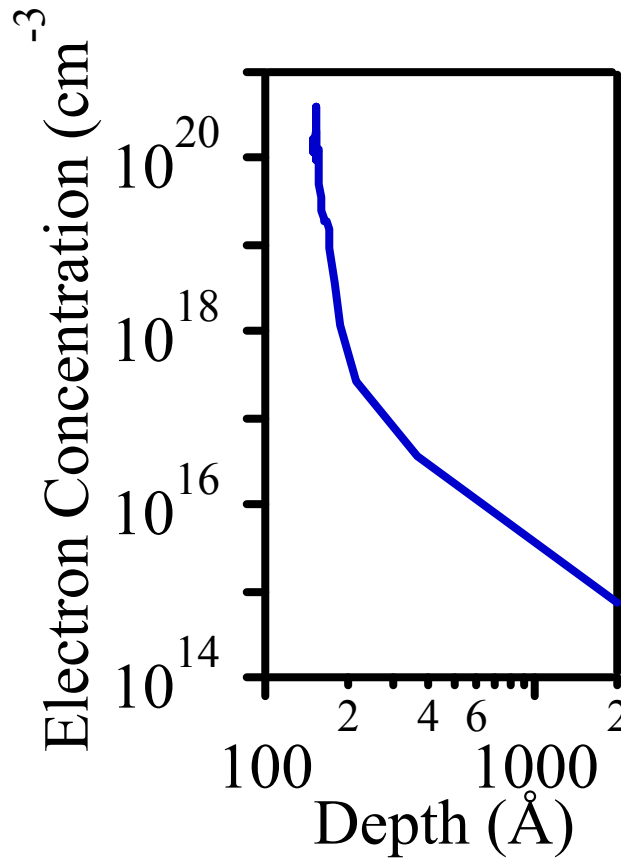
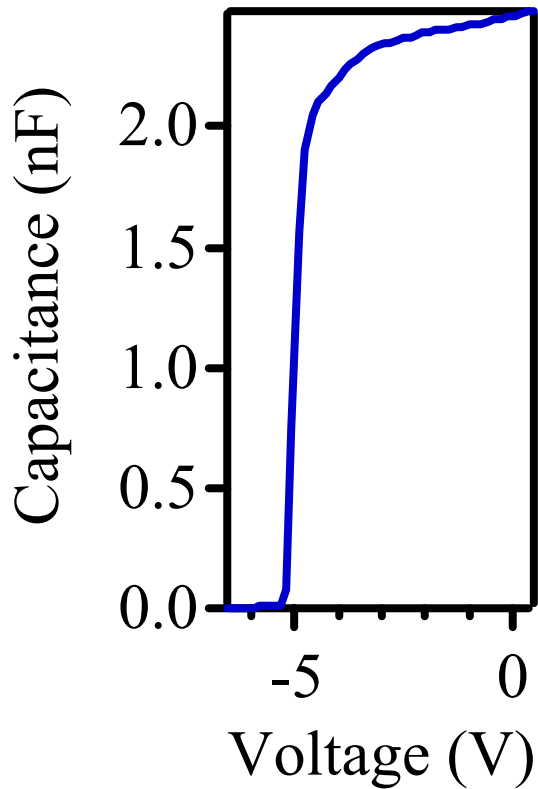


DLTS of unpassivated OMVPE HFET with and without 532nm continuous soak



Sub-bandgap light completely eliminates response of one deep level, while creating a new response not seen in any other GaN HFETs

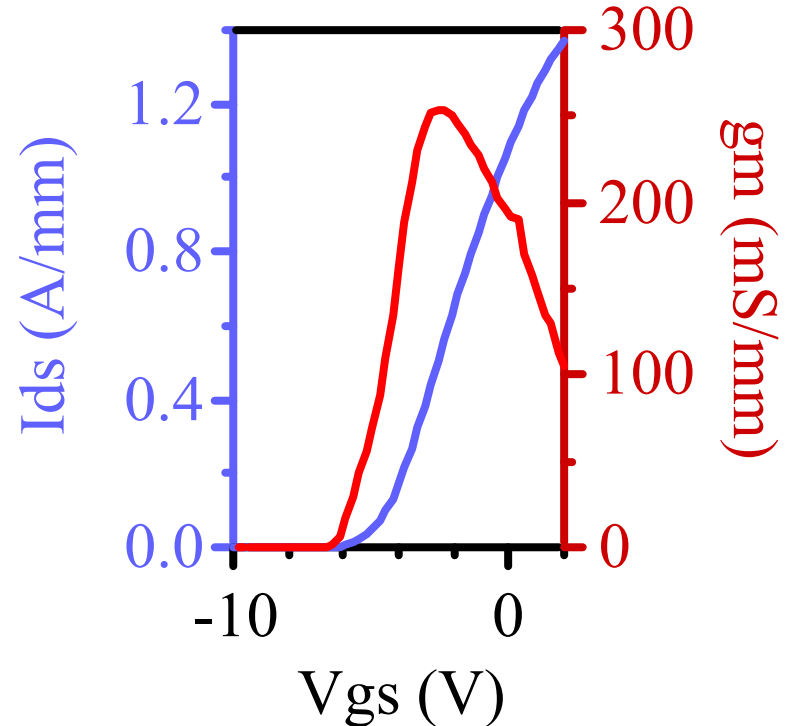
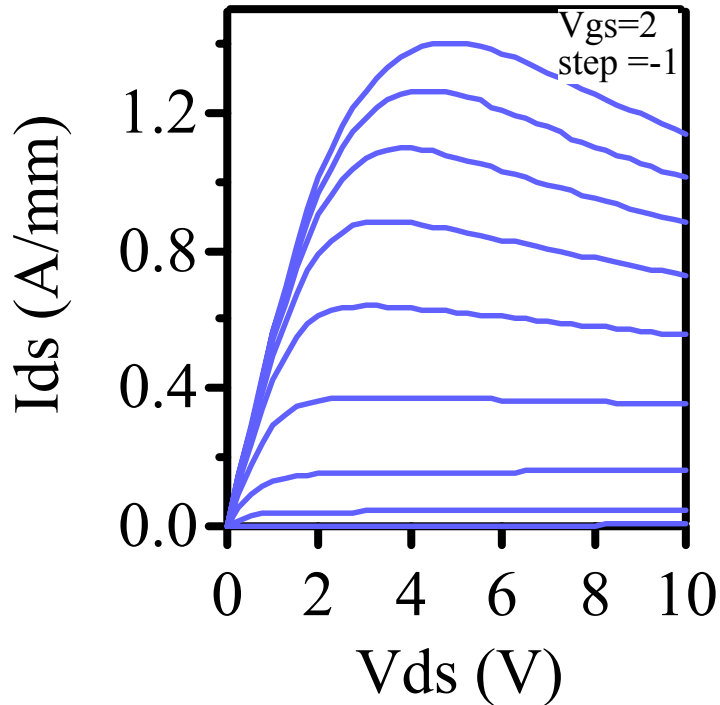
CV and Hall for $\text{Al}_{0.75}\text{Ga}_{0.25}\text{N}/\text{GaN}$ HFET



$\mu = 720 \text{ cm}^2/\text{Vsec}$
 $n_s = 2.5 \times 10^{13} \text{ cm}^{-2}$

Sheet resistivity $350 \text{ }\Omega/\square$

IV for $0.3 \times 100 \mu\text{m}$ $\text{Al}_{0.75}\text{Ga}_{0.25}\text{N}/\text{GaN}$ HFET



Contact Resistance = $0.3 \Omega\text{mm}$
 $f_t = 55$ GHz



MURI wrap-up

Optimize AlN Passivation Process

Perform DLTS comparison of passivated/unpassivated HFETs

Optimize growth of High Al mole fraction barrier HFETs

Grow High Al mole fractions on hydrogen etched SiC substrates

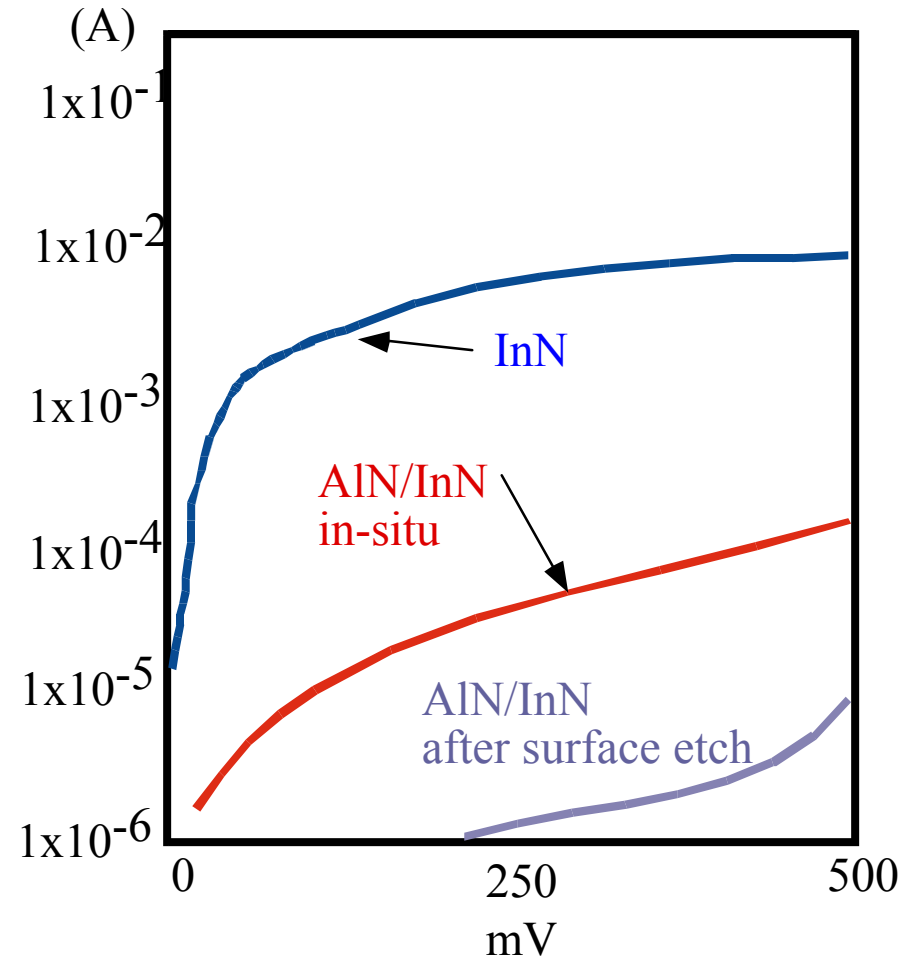
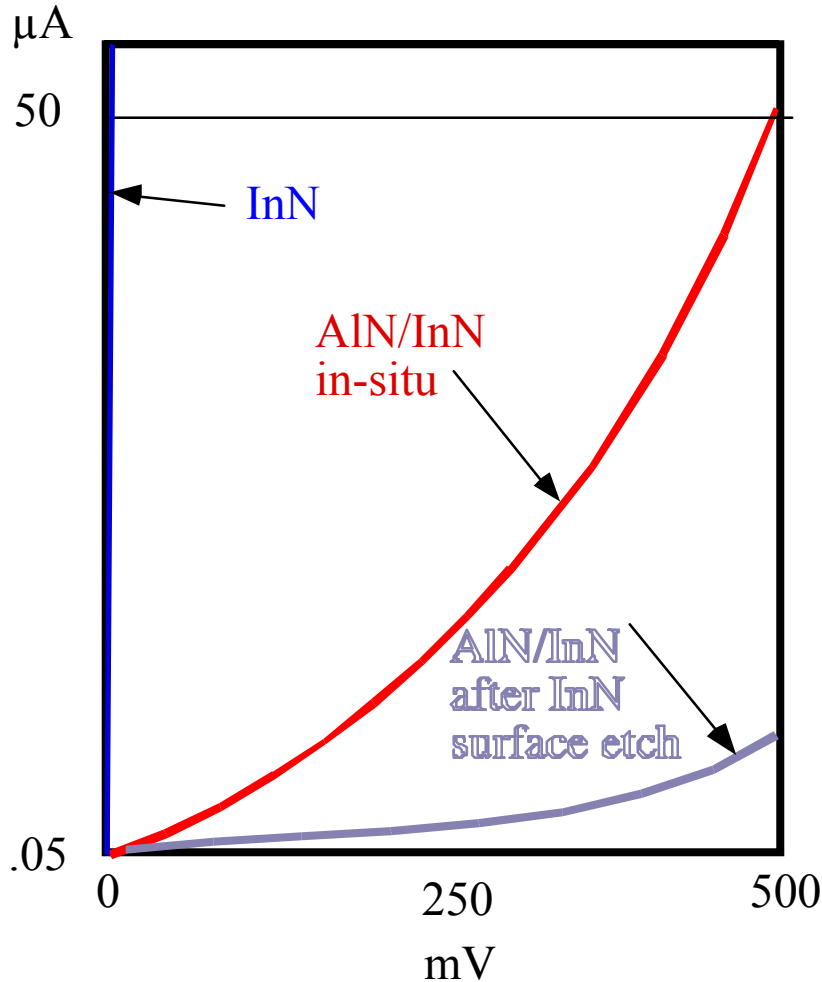
Future Plans

InN channel FETs

High Power, Broadband, Linear, Solid State Amplifier



I-V using Hg probe contacts of AlN/InN with and without surface etch compared to InN surface



Linear and log curves are not the same device, but representative of typical comparison